

UET-02 UNIVERSAL EMBEDDED TRAINER



UET-02 is a single board Universal Trainer Kit which is widely used to train engineers to develop/ Study hardware and software for any large scale integrated circuit application in laboratory. This board having logic inputs, output indicator LED's, Push Switch Key, LCD Display, Matrix Keyboard, Stepper motor, Relay, Buzzer, Rs232, Matrix Display, DC Supplies for experimenting very large scale integration techniques.

Specifications

- Piggy Bag Daughter Board.
- Piggy Bag XILINX SPARTAN XC3S50 FPGA (optional)
- Piggy Bag XILINX SPARTAN XC3S400 FPGA (optional)
- On board USB JTAG Programmer for configuring XILINX Device.
- **Display Indicator**
 - 16 LED's indicator for input/output ports of the FPGA.
 - Six digit seven segment display.
 - 16x2 Alpha-numeric LCD Display with the backlight.
 - Two Dices LED's (Dice-1 and Dice-2)
 - 8x8 Dot Matrix Display.
- **Switches**
 - 2nos. of 8 pin DIP Switches for input selection.
 - 8 Push button Switches with LED indicator.
 - 3 Pulse Generator Switches for High to Low

transition with LED indicator.

- 3 Pulse Generator Switches for Low to High transition with LED indicator.
- 5x4 matrix key board.
- **Onboard Interfaces**
 - RS-232C Serial connector.
 - Miniature Buzzer.
 - 12V SPDT Relay.
 - Stepper Motor Controller.
 - On-board Reset Key.
 - 1Ch. 8 bit ADC using ADC 0804.
 - Two 8 bit DAC using DAC 0800.
 - Temperature sensor interface using Lm35.
- **Clock Generator**
 - On-board 20MHz Oscillator.
 - 20 MHz clock and one of nine different frequency clocks (1Hz, 10Hz, 100Hz, 1 KHz, 10 KHz, 100 KHz, 1 MHz, 10 MHz & 20 MHz).
- Four set of 20x2 Female berg connector to plug-in the DAUGHTER BOARD.
- Supply Input Voltage: 230VAC.
- All IC's are mounted on IC Sockets.
- Bare board Tested Glass Epoxy SMOBC PCB is used.
- Attractive Metal/ Wooden enclosure.
- User's Manual with 'VHDL' Source code sample programs for all on board applications.

Note : Specifications can be altered without notice in our constant efforts for improvement.